

# **INFORMATION DISCLOSURE CITATION**

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APPLICANT(S)

**William P. Moore, et al.**

FILING DATE

**March 14, 2001**

GROUP

**2184**

## **U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
YLW	2003/0070013 A1	04/10/03	Hansson	—	—	—
YLW	6,397,710 B1	05/28/02	Dean, et al.	—	—	—

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## **FOREIGN PATENT DOCUMENTS**

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
					YES	NO

## **OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

YLW	Ching-Long Su, et al., "Low Power Architecture Design and Compilation Techniques for High-Performance Processors", 1994, Pages 489-498.

EXAMINER

*Golanda J. Wilson*

DATE CONSIDERED

*3-24-04*

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.